

Sinhgad Technical Education Society's

Sinhgad College of Engineering Vadgaon (Budruk) Off. Sinhgad Road,S.No. 44/1,

Mrs. Runal P. Pawar M.Tech IT runalppawar.scoe@sinhgad.edu 8220291544



Academ	ic Qualification		r			
Degree	Institute Name	University Name	Year of Passing	Percentag e	Grade	Qualificat ion Status
M.Tech IT	B.V.D.U.C.O.E. Pune	B.V.D.U. Pune	2018-19	9.09/10	1 st Class with Distinction	Completed
B.Tech IT	Avinashilingam university Coimbatore	Avinashiling am university Coimbatore	2015-16	6.5/10	1 st Class	Completed
HSC	Shri Renuga Vidhyalayam Matric.Hr.Sec School Lakshmipuram	Department Of Government Examination Chennai	2012	83%	1 st Class with Distinction	Completed
SSCL	Shri Renuga Vidhyalayam Matric.Hr.Sec School Lakshmipuram	Department Of Government Examination Chennai	2010	81%	1 st Class with Distinction	Completed

Subject Expertise

Subject	No Of Years	Remarks
Software Testing	01(Visiting)	

Training Details

Train Title	Arrange By	Venue	Date	Level	Category	Туре

Name	Venue	Date	Level	Category	Туре	Sponsor ed By
2 nd Internation al Conferenc e on Advanced Technologi es for Research and Product Developme nt	Kathir College of Engineering, Coimbatore.		International	Research		Kathir College of Engineeri ng, Coimbato re

Workshop Attended

Workshop Topic	Conducted By	Date	Level	Category	Туре
Networking & Cyber Security	Computer Engineering Department of Bharati Vidyapeeth College of Engineering, Navi Mumbai	23 rd to 24 th April 2021	Extra Curricular Activities	Extra Curricular Activities	External
Artificial Intelligence	Bharati Vidyapeeth College of Engineering, Department of Computer Engineering in collaboration with the National Youth Council of India and Brainovision Solutions India Pvt. Ltd.	22 nd May to 26 th June 2020	Quality Improvem ent Program	Quality Improve ment Program	External

Research Projects

Project name	Description	Status	Budget	Project type

ISSN /ISB N No	Paper Title	Publis h Date	Journal/Conference Name	Level	Paper Status	Authors
0976-63 59	Functional Test Case Generation and Redundancy Removal Based On Model Driven Testing Using UML Activity Diagram	May 2019	International Journal of Mechanical Engineering and Technology (IJMET)	Indexed Journal	Published	Runal

2249-89 58	UML Activity Diagram Use for Functional Test Suite Generation and Redundancy Removal Supported Model Driven Testing	August 2019	International Journal of Engineering and Advanced Technology (IJEAT)	Indexed Journal	Published	Runal
2321-96 53	Model Driven Testing-Functiona I Test Case Generation with Redundancy Check and Model Paradigm Approach	June 2019	International Journal for Research in Applied Science and Engineering Technology (IJRASET)	International	Published	Runal

Membership Details

Membership Body	Details	Duration	Level

Students Registered for Ph.D

Student Name	Торіс
-	-

Books Published

ISBN	Title	Publisher	Publish Year

Declaration

I hereby declare that all the above information furnished by me is true to the best of my knowledge.

Date: 15/ Sep/2022

Mrs. Runal P. Pawar